



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

2182
#7
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a-23-24

In re application of: William J. Armstrong et al. : Date: January 13, 2004
Group Art Unit: 2182 : IBM Corporation
Examiner: Unassigned : Intellectual Property Law
Serial No.: 09/838,057 : Dept. 917, Bldg. 006-1
Filed: April 19, 2001 : 3605 Highway 52 North
Title: METHOD AND APPARATUS FOR : Rochester, MN 55901
ALLOCATING PROCESSOR
RESOURCES IN A LOGICALLY
PARTITIONED COMPUTER
SYSTEM

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants request that the information listed on the attached Form PTO/SB/08A be considered by the Office during the pendency of the above entitled application, pursuant to 37 C.F.R. §1.97. In accordance with 37 C.F.R. §1.97(h), the filing of this Information Disclosure Statement shall not constitute an admission that any information cited therein is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b). In the interest of full and complete disclosure to the Office, some or all of the art cited herein may not be considered by Applicant(s) or the Undersigned to be material under the new standard of materiality defined in 37 C.F.R. §1.56(b), enacted March 16, 1992, but may be material under the old standard of materiality defined in 37 C.F.R. §1.56(a), last amended on November 28, 1988, or may merely be technical background which may be of interest to the Examiner. In accordance with

Serial No. 09/838,057
Docket No. ROC920000252US1

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37 C.F.R. §1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made.

To the best knowledge of the undersigned, this Information Disclosure Statement is being filed under 37 C.F.R. §1.97(b)(3) before the mailing of a first Office action on the merits in the present application. Accordingly no filing fee is required.

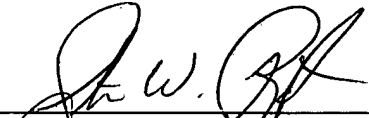
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on

January 13, 2004
(Date of Deposit)

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Respectfully submitted,

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PTO/SB/08A


**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
Sheet 1 of 2

Application Number.: 09/838,057
 Filing Date: April 19, 2001
 First Named Inventor: Armstrong et al
 Art Unit: 2182
 Examiner Name: Unassigned
 Attorney Docket Number.: ROC920000252US1

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	<u>Document Number</u>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns or Lines Where Relevant Passages or Figs. Appear
		Number - Kind Code ² (if known)			
	A	US - 5535321A	07/09/1996	Timothy J. Massaro et al	
	B	US - 6625638B1	09/23/2003	Jeffrey Paul Kubala et al	

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FOREIGN PATENT DOCUMENTS

Examiner Initials	Cite No. ¹	<u>Foreign Patent Document</u>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns or Lines Where Relevant Passages or Figs. Appear	T ⁶
		Country ³ - Number ⁴ - Kind Code ⁵ Code (if known)				

Examiner Signature		Date Considered	
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² See Kind Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3).

⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language translation is attached.

PTO/SB/08A

**INFORMATION CONCERNING
STATEMENT BY APPLICANT**

Sheet 2 of 2

Application Number.: 09/838,057
 Filing Date: April 19, 2001
 First Named Inventor: Armstrong et al
 Art Unit: 2182
 Examiner Name: Unassigned
 Attorney Docket Number.: ROC920000252US1

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and or country where published.	T ²
	C	Menasce, D. et al. "Capacity Planning and Performance Modeling", ISBN 0-13-035494-5, © 1994.	
	D	Leutenegger et al. "A Modeling Study of the TPC-C Benchmark", Proceedings of the 1993 ACM SIGMOD Int'l Conference on Management of Data, 1993, pp. 22-31.	
	E	Levine, C. "Order-of-Magnitude Advantage on TPC-C Through Massive Parallelism", Proceedings of the 1995 ACM SIGMOD Int'l Conference on Management of Data, 1995, pp. 464-465.	
	F	IBM Corporation, "AS/400 Logical Partitions Hardware Planning Guide", © 1999	
	G	Schimunek, G. et al. "Slicing the AS/400 With Logical Partitioning: A How to Guide", August 1999.	
	H	IBM Corporation, "LPAR Configuration and Management", First Edition, © April 2002.	
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